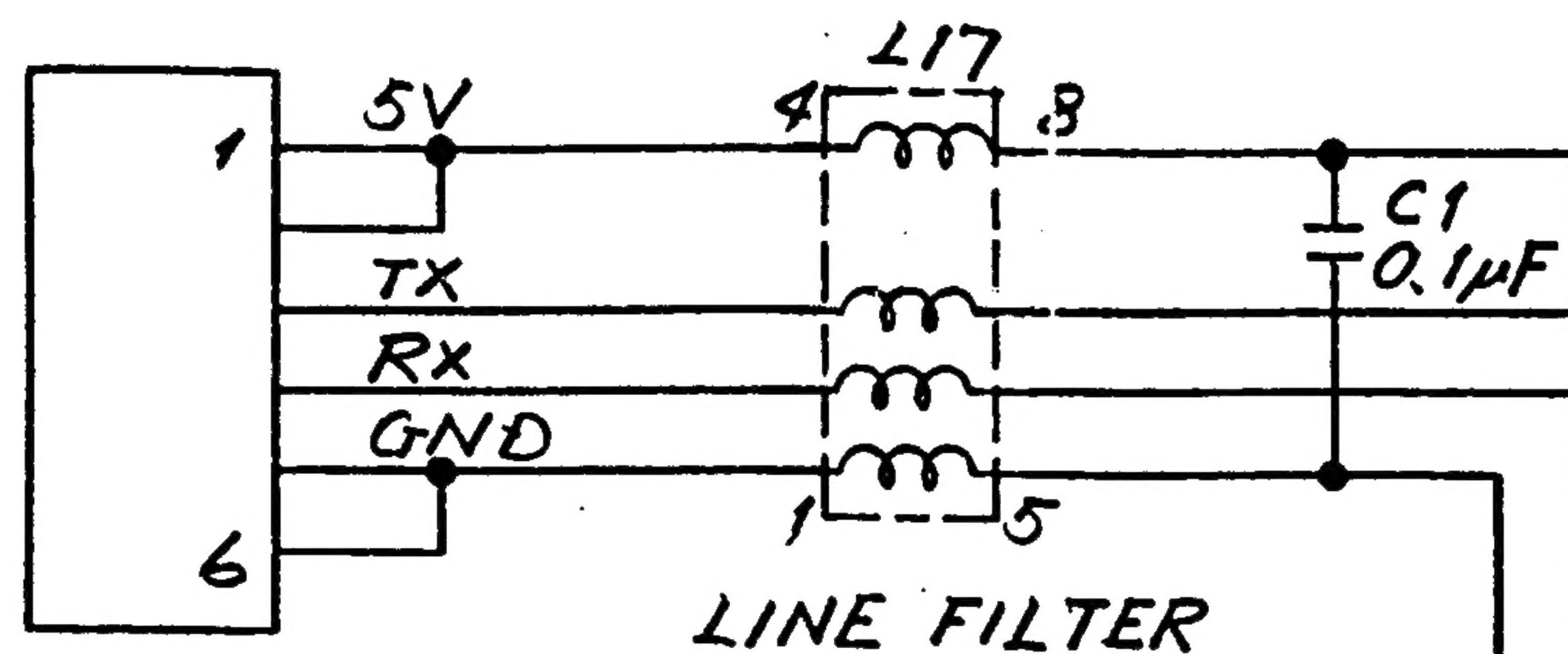
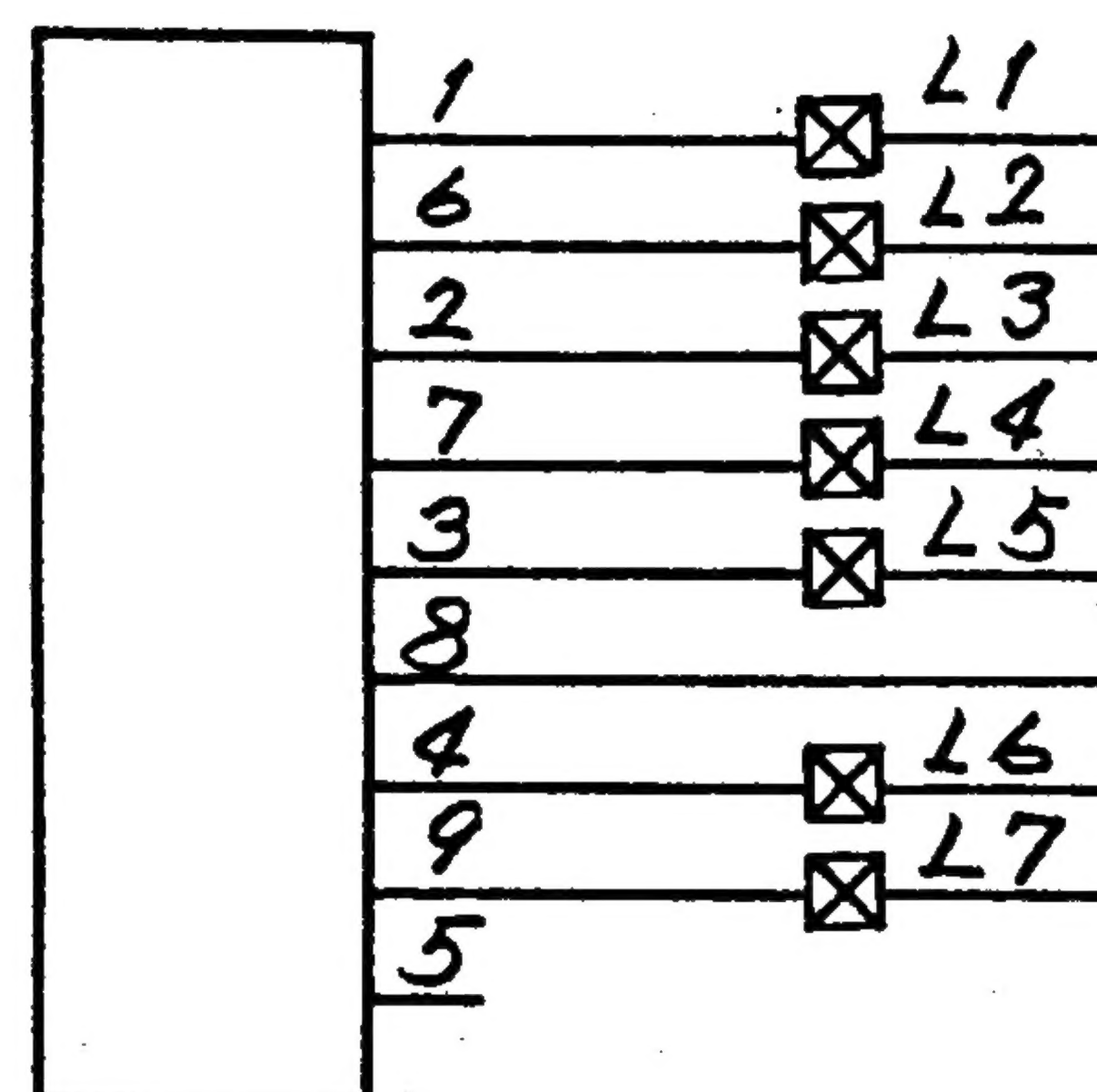


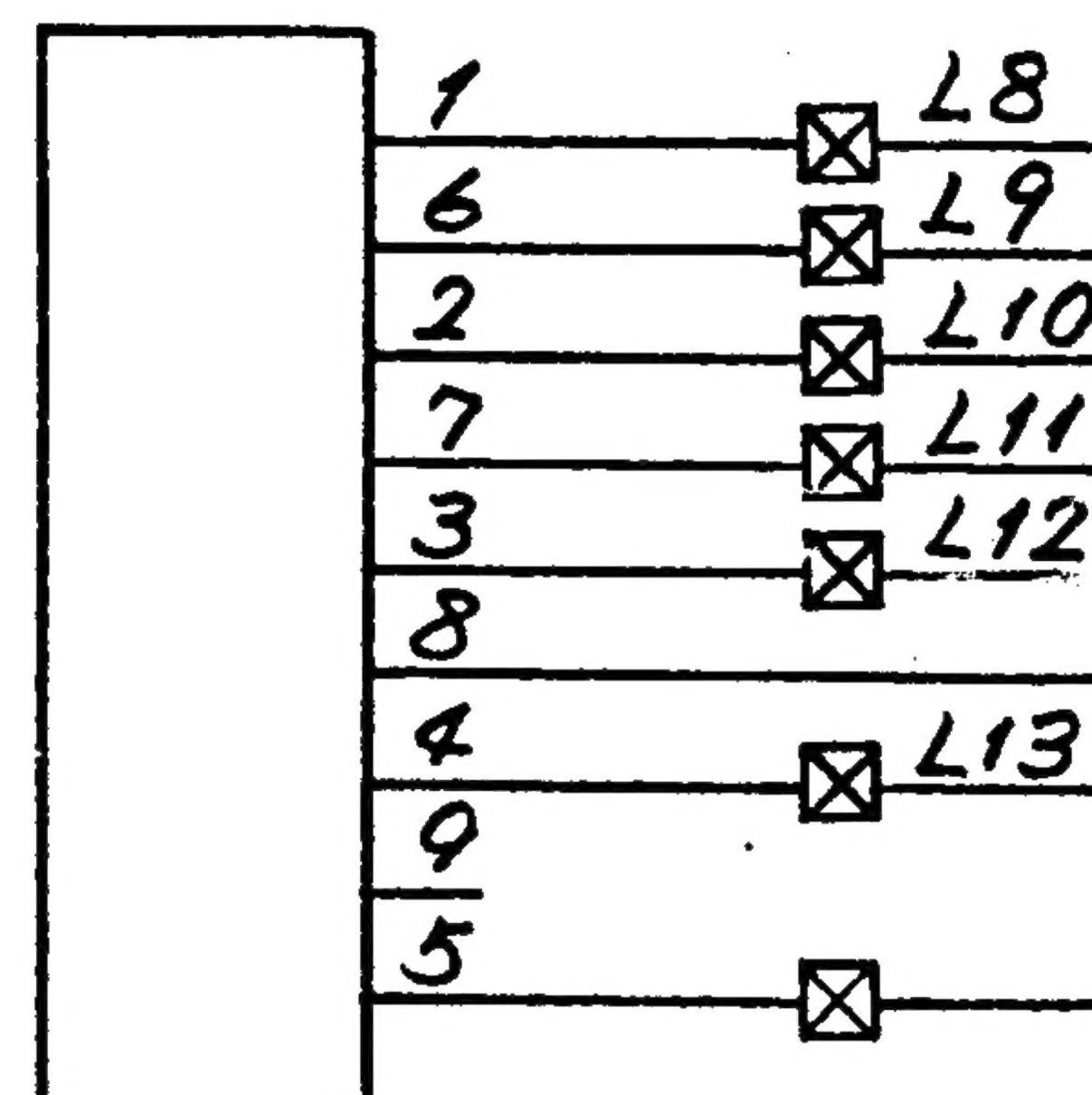
J1
MAIN



J2
JOY 0



J3
JOY 1



J4
KEY BOARD

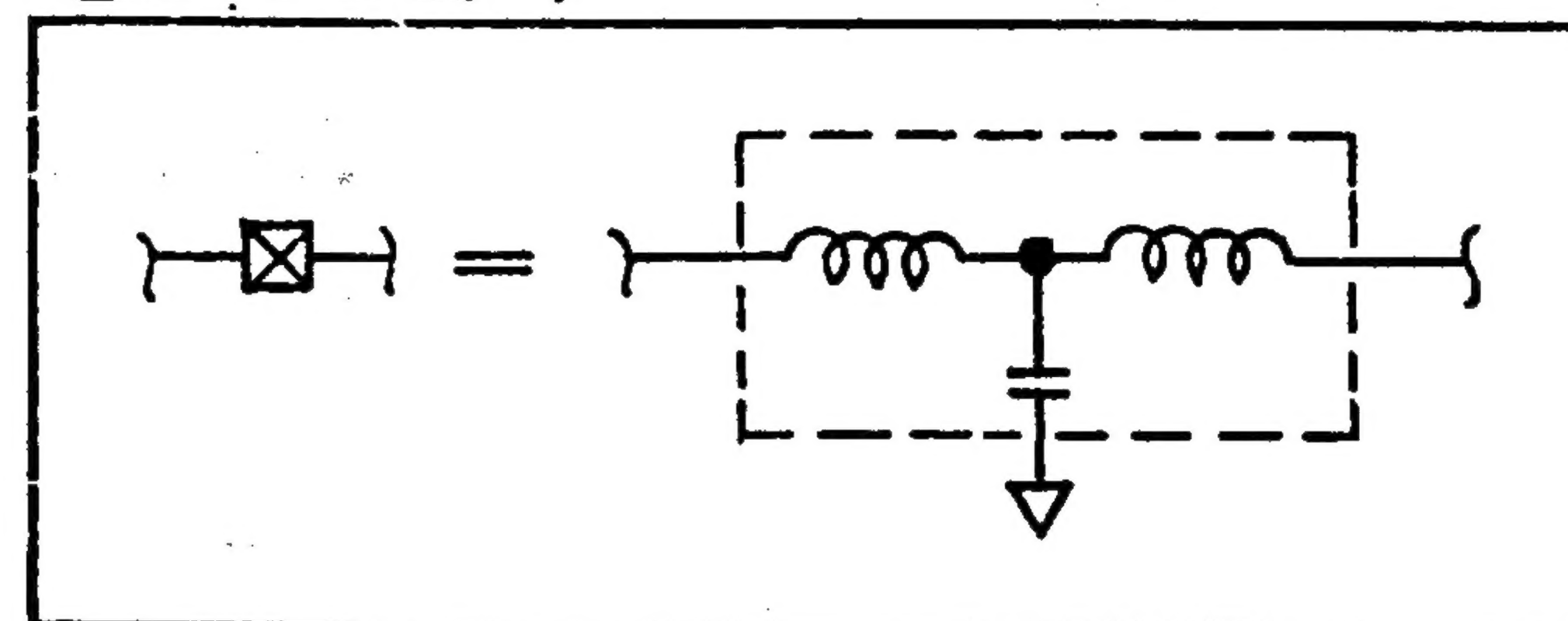
18	NC
17	JOY 1-5
16	NC
15	SD FROM CPU
14	SD TO CPU
13	VCC +5V
12	JOY 0-1
11	JOY 0-6
10	JOY 0-2
9	JOY 0-3
8	JOY 0-4
7	JOY 1-1
6	JOY 1-6
5	JOY 1-2
4	JOY 1-3
3	JOY 1-4
2	KEY
1	GND

T x DATA
R x DATA
+5V
XB/UP
L BUTTON/FIRE BUTTON
XA/DOWN
YA/LEFT
YB/RIGHT
UP
R BUTTON/FIRE BUTTON
DOWN
LEFT
RIGHT
KEY
GND

REV	REVISIONS DESCRIPTION	DATE	APPROVED
1	PRELIMINARY RELEASE	2/19/87	JH
2	DELETE RES. & CAP. (R1, C2)	1-9-87	
3	REVISED	2/19/87	JH

REFERENCE ONLY

DETAIL 1



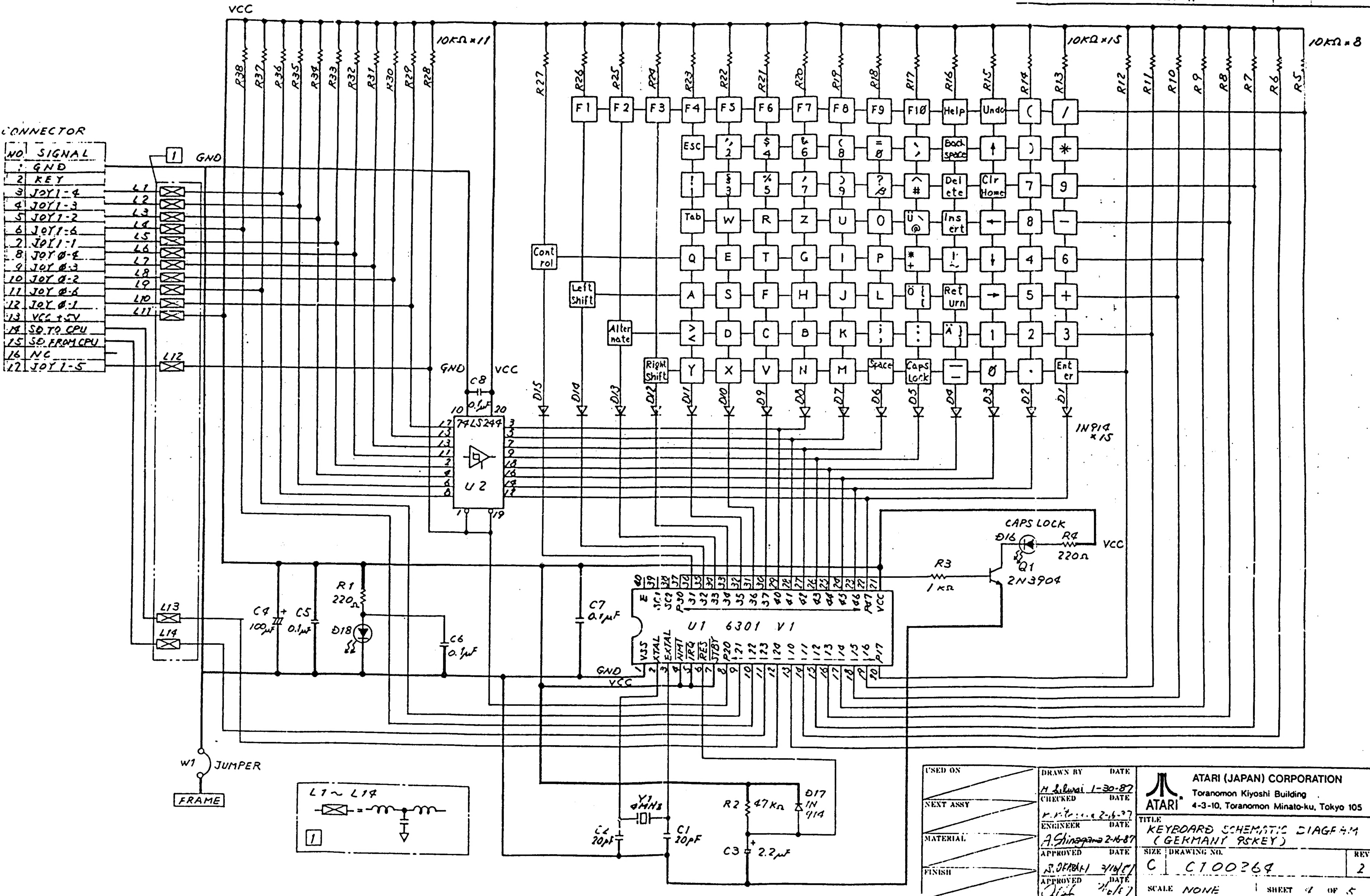
2. USE SUPPLIED P.C.B. C100063, REV. 1
1. NOISE FILTER: SEE DETAIL 1.

NOTES—UNLESS OTHERWISE SPECIFIED:

TOLERANCES		DRAWN BY	DATE	ATARI (JAPAN) CORPORATION Toranomon Kiyoshi Building 3F 4-3-10, Toranomon Minato-ku, Tokyo 105	
UNDER 30	±0.1	I. Yoshida	10-24-86		
30 THRU 300	±0.2	CHECKED	DATE	TITLE SCHEMATIC DIAGRAM FOR SUB P.C.B. (MEGA ST KEYBOARD)	
OVER 300	±0.4	K. Kitagawa	10-27-86		
MATERIAL		ENGINEER	DATE	SIZE	DRAWING NO.
CA200042-XXX MEGA ST KEYBOARD		A. Shinagawa	2-16-87	B	C100069
		APPROVED	DATE	REVISION	
NEXT ASSY		S. Okazaki	2/18/87	3	
USED ON		APPROVED	DATE	SCALE	SHEET 1 OF 1
APPLICATION		JH	2/19/87	NONE	

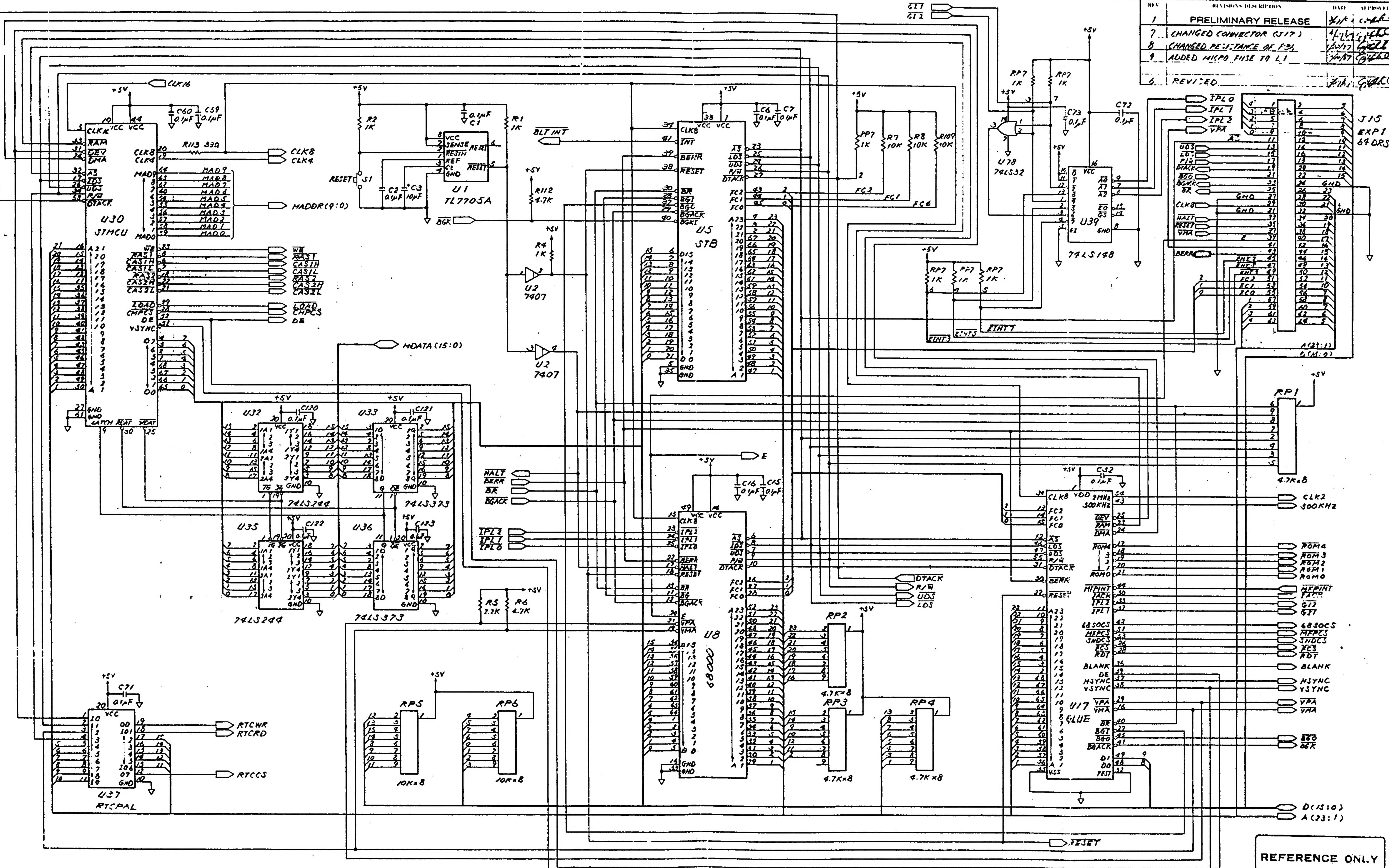
CONNECTOR

NO.	SIGNAL
1	GND
2	KEY
3	JOY1-4
4	JOY1-3
5	JOY1-2
6	JOY1-1
7	JOY0-4
8	JOY0-3
9	JOY0-2
10	JOY0-1
11	JOY0-0
12	JOY0-1
13	VCC +5V
14	SD TO CPU
15	SD FROM CPU
16	NC
17	JOY1-5

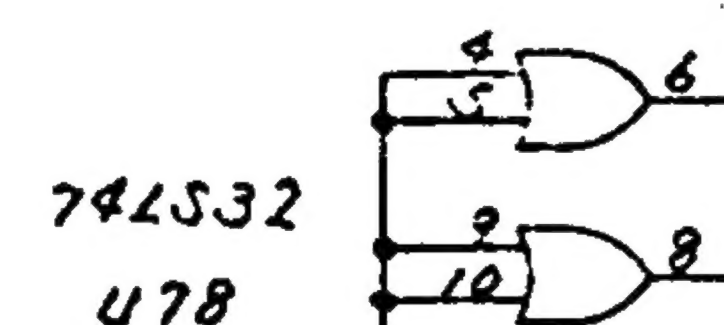


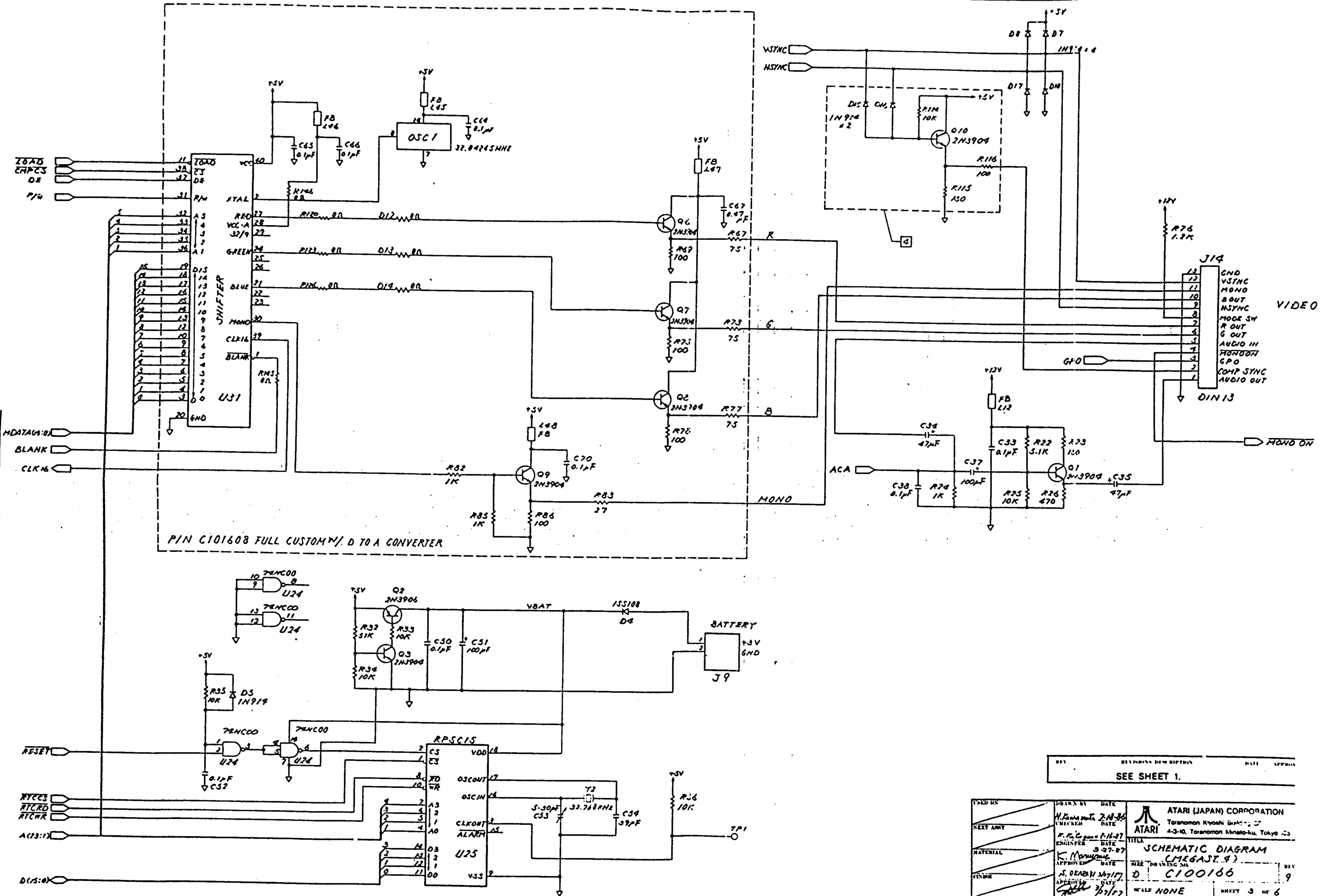
USED ON NEXT ASSY MATERIAL FINISH	DRAWN BY H. Shibata 1-30-87	DATE 1-30-87	ATARI (JAPAN) CORPORATION Toranomon Kiyoshi Building 4-3-10, Toranomon Minato-ku, Tokyo 105 TITLE KEYBOARD SCHEMATIC DIAGRAM (GERMAN 95KEY) SIZE DRAWING NO. C C100264 SCALE NONE SHEET 1 OF 5
	CHECKED K. S. ... 2-16-87	DATE 2-16-87	
	ENGINEER A. Shingawa 2-16-87	DATE 2-16-87	
	APPROVED J. OKADA 3/14/87	DATE 3/14/87	
	APPROVED [Signature] 3/16/87	DATE 3/16/87	

REV	REVISION DESCRIPTION	DATE	APPROVED
1	PRELIMINARY RELEASE	4/1/86	Y. K.
7	CHANGED CONNECTOR (J17)	4/1/86	Y. K.
8	CHANGED RESISTANCE OF F31	4/1/86	Y. K.
9	ADDED MICRO FUSE TO L1	4/1/86	Y. K.
6	REVISED	4/1/86	Y. K.




1. ROM TABLE : SEE SHEET 5 OF 6
2. NOT REQUIRED FOR Assy NO. CA2000SS-XX1.
3. PUT JUMPER WIRE FROM (A) TO (B) (IN DETAIL A' L2~4, 6~8, 13~22, 24~30, 32~43)
Assy NO. CA2000SS-1A2
4. NOISE FILTER : SEE DETAIL 1





SEE SHEET 1.

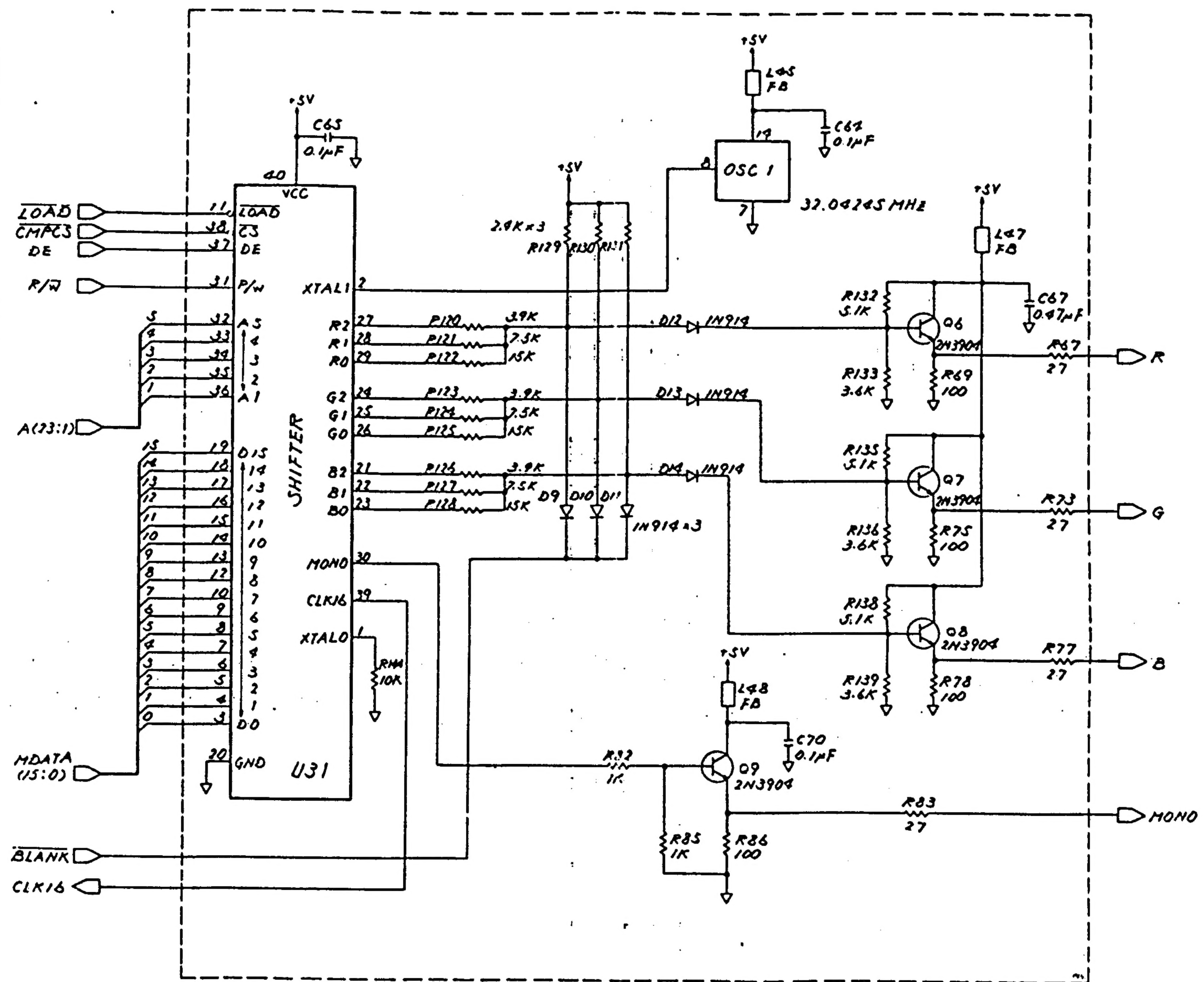
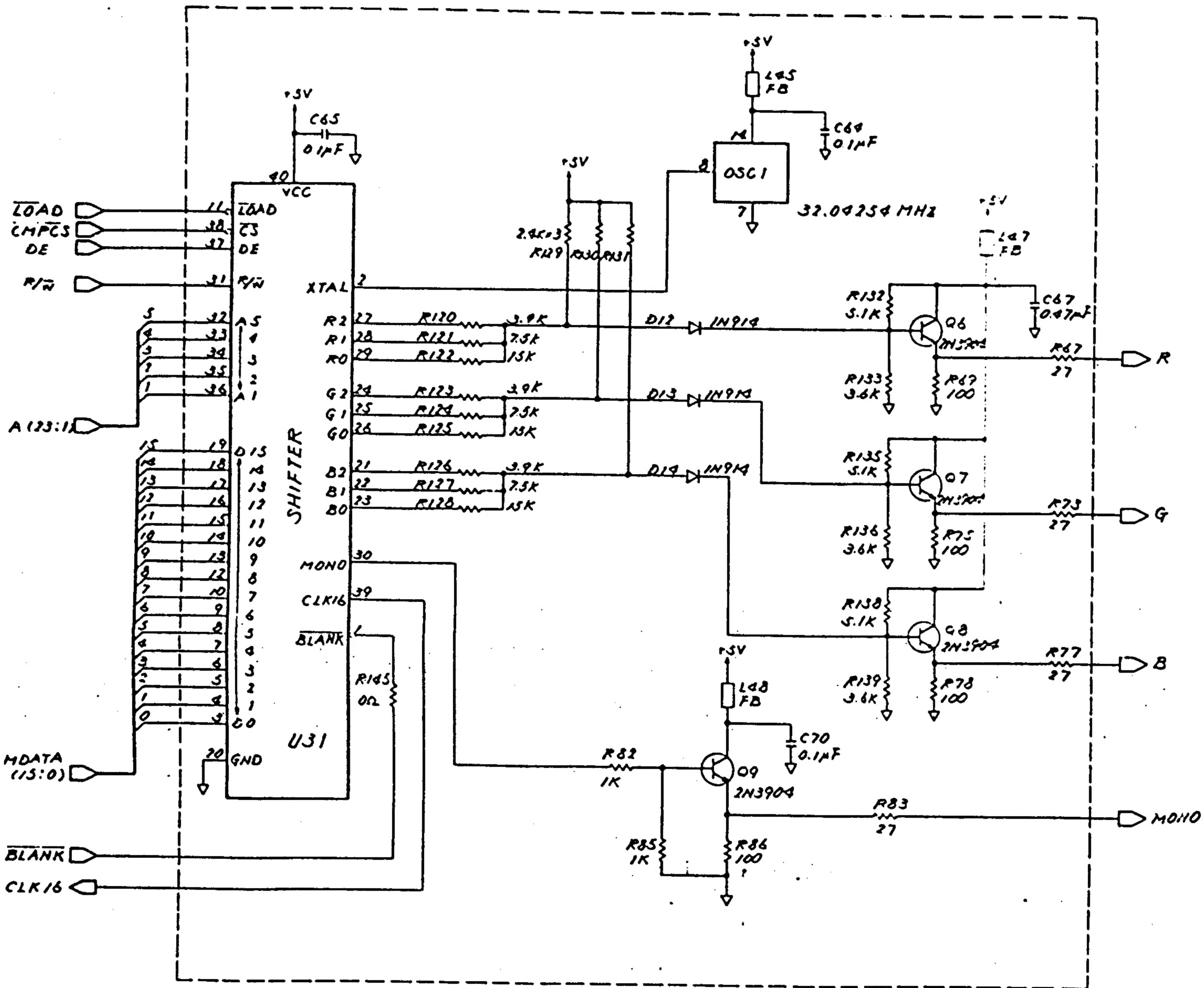

ATARI (JAPAN) CORPORATION
 Teranomon Kyosai Bldg. 5F
 4-3-10, Teranomon Minato-ku, Tokyo 106

7	TITLE	SCHEMATIC DIAGRAM
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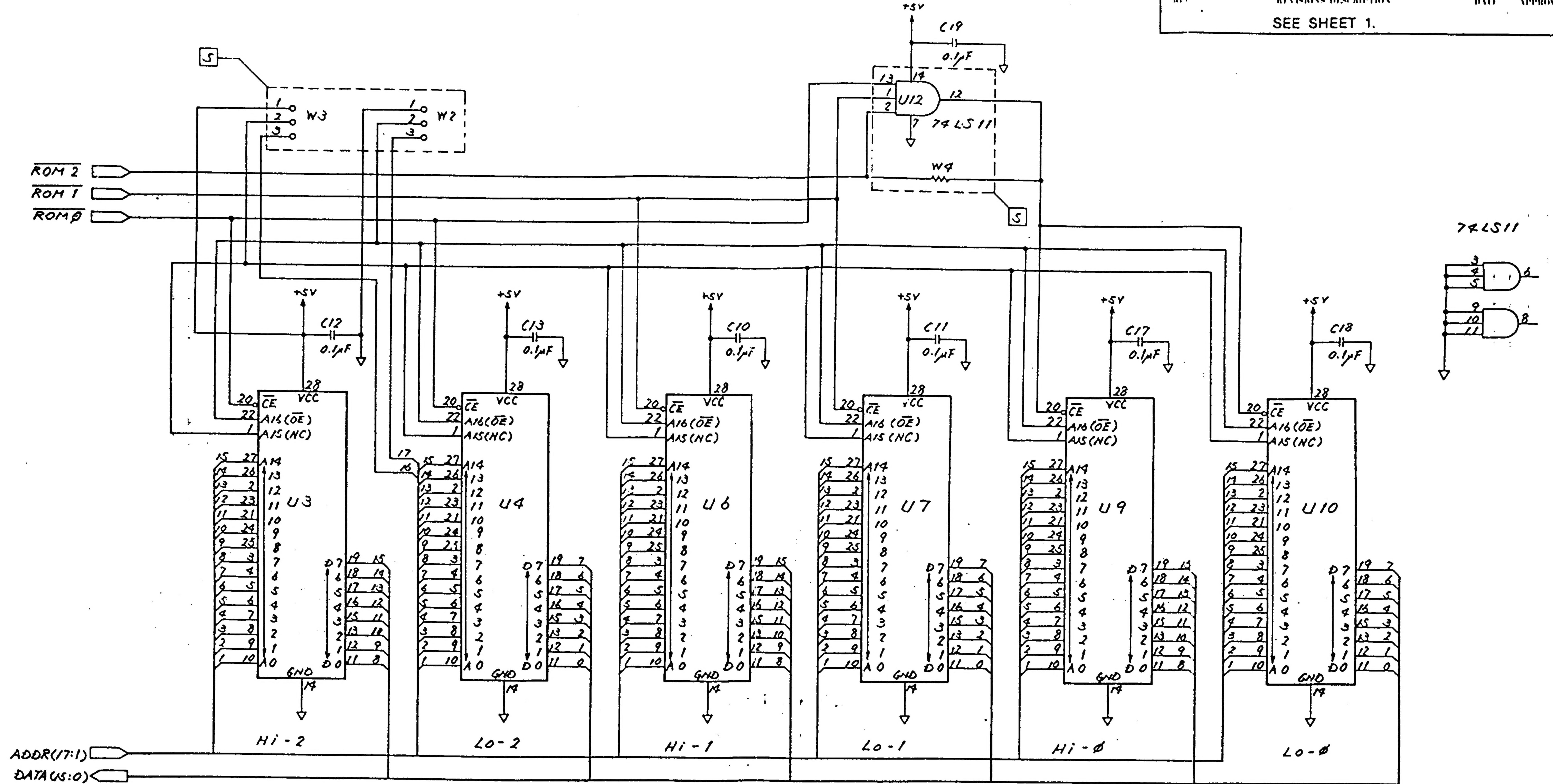
MEGA 8
DRAWING NO. C100166

100

DATE	NONE	UNIT	3	W	6
------	------	------	---	---	---



USED ON	DRAWN BY	DATE	ATARI (JAPAN) CORPORATION Toranomon Kyosha Building 3c 4-3-10, Toranomon Minato-ku, Tokyo 105
NEXT ASSY	HECKED	DATE	
MATERIAL	ENGINEER	DATE	
FINISH	APPROVED	DATE	
	APPROVED	DATE	
H. Kawasumi 3-5-87 K. K. K. 3-11-87 K. K. K. 3-27-87 P. K. K. 10/7/77			TITLE SCHEMATIC DIAGRAM (MEGAST 4) SIZE D C100166 SCALE NONE
			SHEET 4 OF 6



5

ROM TABLE

IC

; U12 (74LS11)

JUMPER

; W2, W3, W4

ROM	LOCATION			
	W2	W3	W4	U12
256K	1-2	1-2	JUMPER	NO USE
1M x 2	2-3	2-3	NON	USE
1M x 6	2-3	2-3	JUMPER	NO USE

USED ON	DRAWN BY	DATE	ATARI (JAPAN) CORPORATION	
SENT ASSY	<i>N. Yamamoto</i>	11-22-86	Toranomon Kiyoshi Building 3F	
MATERIAL	CHECKED	DATE	4-3-10, Toranomon Minato-ku, Tokyo 105	
FINISH	<i>K. Kikuchi</i>	2-16-87	TITLE	
	ENGINEER	DATE	SCHEMATIC DIAGRAM	
	APPROVED	DATE	(MEGAST 8)	
	<i>S. Okazaki</i>	3/27/87	SIZE	DRAWING NO.
			C	C100166
			SCALE	NONE
			SHEET	5 OF 6

